

ASSISTANT COMMISSIONER FOR PATENTS

Washington, D.C. 20231

12/14/01
Dear Sir:

Transmitted herewith for filing is the patent application of:

Inventor(s): Tomoyuki FURUHATA

For: SEMICONDUCTOR DEVICES HAVING A NON-VOLATILE MEMORY
TRANSISTOR AND METHODS FOR MANUFACTURING THE SAME10/014584 PTO
12/14/01

Enclosed are:

26 Pages of text and 16 Sheet(s) of drawings (formal informal) + 0 extra copies

A certified copy of 2000-382396, filed 15 December 2000, in Japan, from which priority is claimed in the subject case pursuant to Rule 55(b) and 35 USC 119. Will follow.

 Declaration and Power of Attorney. Will Follow.

 Information Disclosure Statement including PTO Form 1449 and copies of references.

CALCULATION OF FEES					
ITEM		NUMBER OF CLAIMS FILED MINUS BASE*	NUMBER OF CLAIMS OVER BASE x RATE SM/LG ENTITY	\$ AMOUNT	\$ FEE
A	TOTAL CLAIMS FEE	27 - 20* =	x \$9.00 or 7 x \$18.00	\$126	
B	INDEPENDENT CLAIMS FEE**	9 - 3* =	x \$42.00 or 6 x \$84.00	\$504	
C	SUBTOTAL - ADDITIONAL CLAIMS FEE (ADD FINAL COLUMN IN LINES A + B)			\$630.00	
D	BASIC FEE - SMALL ENTITY FEE = \$370.00 / LARGE ENTITY FEE = \$740				\$740.00
E	MULTIPLE-DEPENDENT CLAIMS FEE - SMALL ENTITY FEE = \$140.00 / LARGE ENTITY FEE = \$280				\$0
F	ASSIGNMENT RECORDING FEE \$ 40.00				\$0
G	TOTAL FEE (ADD LINES C, D, E AND F)				\$1370.00
	**LIST INDEPENDENT CLAIMS 1, 6, 10, 11, 15, 23, 24, 25, 27				

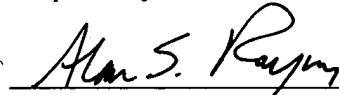
A check in the amount of \$ 1370.00 to cover the filing fee is enclosed. A copy of this letter is enclosed.

The Commissioner is hereby authorized to charge payment of the following fees associated with this communication or credit any overpayment to Deposit Account No. 50-0585. A duplicate copy of this sheet is enclosed.

Any additional filing fees required under 37 CFR 1.16.

Any patent application processing fees under 37 CFR 1.17.

Respectfully submitted,



 Alan S. Raynes, Reg. No. 39,809
Date: December 14, 2001
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15.57/6348

IN THE UNITED STATES PATENT AND TRADEMARK OFFICE

In re the Application of
Tomoyuki FURUHATA

Serial No.: To Be Assigned

Filing Date: December 14, 2001

For: SEMICONDUCTOR DEVICES HAVING
A NON-VOLATILE MEMORY
TRANSISTOR AND METHODS FOR
MANUFACTURING THE SAME

CERTIFICATE OF DEPOSIT

Assistant Commissioner for Patents
Washington, DC 20231

Date of Deposit: December 14, 2001

I hereby certify that a utility patent application and patent application fee transmittal; patent application including 26 pages of text (including an abstract and 27 claims), 16 sheets of drawings; an \$1370 check for the filing fee; and a certified copy of application number 2000-382395, filed 15 December 2000, in Japan; are being deposited with the United States Patent & Trademark Office on the date indicated above.

December 14, 2001
Date of Deposit

Alan S. Raynes
Name of person delivering papers

Alan S. Raynes
Signature